Skip With BBT 7ByteECC User Manual

General Description and Name

This scheme is implements the skip method and write bad block table and bad block table mirror in the second and third logic blocks(block number starts at 0).

Relevant User Options

The following special features on the special features tab apply to this scheme. The default values might work in some cases but please make sure to set the right value according to your system.
Please note only the below special feature items are related to this scheme and ignore any others. If any of below items doesn't exist, please check whether the right version has been installed or contact Data I/O for support by submitting Device Support Request through this address:
http://www.dataio.com/support/dsr.asp

Bad Block Handling Type = "Skip With BBT 7ByteECC"

Spare area : Please refer to "Description of common NAND special features.pdf". ***Normally set as "Enabled" for this BBM***.[Default 'Disabled']

Check BadBlock Marker in Data File: Please refer to "Description of common NAND special features.pdf". ***Normally set as "Disabled" for this BBM***.[Default 'Enabled']

bad block detection: Please refer to "Description of common NAND special features.pdf". ***Normally set as "BBM then BB marker" for this BBM***.[Default 'semi vendor BB marker']

Special Notes

Please don't put new devices and programmed devices together. If put them together, Programmer will show "program error" .

Revision History

V1.0 Date 2014-10-23
Create this spec.

Appendix

You can get the file "Description of common NAND special features.pdf" from http://ftp.dataio.com/FCNotes/BBM/