Skip iMX51 2Kpage User Manual

General Description and Name

There is a multiple partition header in the beginning of the PC file. And the general method is using Skip Bad Block. Also there are two BBT copies in the last 2 good blocks.

Relevant User Options

The following special features on the special features tab apply to this scheme. The default values might work in some cases but please make sure to set the right value according to your system.
Please note only the below special feature items are related to this scheme and ignore any others. If any of below items doesn't exist, please check whether the right version has been installed or contact Data I/O for support by submitting Device Support Request through this address:
http://www.dataio.com/support/dsr.asp

Bad Block Handling Type =   "Skip iMX51 2Kpage"

Spare area: Please refer to "Description of common NAND special features.pdf". ***Normally set as " ECC iMX51 2Kpage " for this BBM***.[Default 'Disabled']

Bad Block Marker Offset =   default 54. And this indicates that the bad block position swapped to 2048 + 54 in each page.

Special Notes

Please use the special data file with a multiple header as the PC File.

Revision History

V1.0   July 13, 2010
        Create this spec.

Appendix

You can get the file "Description of common NAND special features.pdf" from
http://ftp.dataio.com/FCNotes/BBM/