Skip PalmOne User Manual

General Description and Name

Skip PalmOne. This is a modified version of the Skip Bad Blocks method of handling bad blocks. There is a table located in a page of the first good block within the first 4 blocks.

Relevant User Options

The following special features on the special features tab apply to this scheme. The default values might work in some cases but please make sure to set the right value according to your system.
Please note only the below special feature items are related to this scheme and ignore any others. If any of below items doesn't exist, please check whether the right version has been installed or contact Data I/O for support by submitting Device Support Request through this address:
http://www.dataio.com/support/dsr.asp

Bad Block Handling Type =   "Skip PalmOne"

Spare area :   Please refer to "Description of common NAND special features.pdf". ***Normally set as " Enabled" for this BBM***.[Default 'Disabled']

Required good block area: Start block = "0" Please refer to "Description of common NAND special features.pdf".

Required good block area: Number of blocks = "0" Please refer to "Description of common NAND special features.pdf".

Special Notes

The spare area in this scheme can either be programmed with the customer's image file, or it can be ignored. ECC is not an option with this particular scheme. However, the bad block marks are always located in the spare area. (Byte 517 for x8 devices)

The data file doesn't have to be arranged in any special way for this scheme. The binary that should be placed into the device is all that is needed. However, special care should be taken into account if the spare area option is set to "enabled". In that case, byte 517 of each page (the sixth byte of the spare area in each page) needs to be left at 0xFF. This is because byte 517 is used to identify bad blocks in the device. If you program one of these bytes to something other than 0xFF, there will be no way for anyone to distinguish a factory marked bad block from a block that has had byte 517 programmed by the programmer.

Revision History

V1.0   June 11, 2009
        Create this spec.

Appendix

You can get the file "Description of common NAND special features.pdf" from
http://ftp.dataio.com/FCNotes/BBM/