Skip BB With BBT With BCH4 Ecc User Manual

General Description and Name

This scheme is implements the skip block method and using BCH4 on BCM platform to calculate ECC.

Relevant User Options

The following special features on the special features tab apply to this scheme. The default values might work in some cases but please make sure to set the right value according to your system.
Please note only the below special feature items are related to this scheme and ignore any others. If any of below items doesn't exist, please check whether the right version has been installed or contact Data I/O for support by submitting Device Support Request through this address:
http://www.dataio.com/support/dsr.asp

Bad Block Handling Type =   "Skip BB With BBT With BCH4 Ecc"

Spare area =   "ECC"
[Default 'Disabled']

BB: max allowed =   The Max permitted number of bad blocks. Please note, if the actual bad block number of the chip is bigger than this value, the algorithm error will be reported. This value is decimal.
[Default '80']

Special Notes

None.

Revision History

V1.0   Date: 2016-03-31
      Create this spec.

Appendix

You can get the file "Description of common NAND special features.pdf" from
http://ftp.dataio.com/FCNotes/BBM/