Monahans_FX41_MIX User Manual

General Description and Name

This is a mixed BBM with Monahans and FlashFX4.1. In general, Monahans manages the first 6 blocks and the last 5 blocks, other blocks are all managed by FlashFX4.1. This BBM also implements the NAND internal ECC, so it can only work with the chips which have this feature.

Relevant User Options

The following special features on the special features tab apply to this scheme. The default values might work in some cases but please make sure to set the right value according to your system.
Please note only the below special feature items are related to this scheme and ignore any others. If any of below items doesn't exist, please check whether the right version has been installed or contact Data I/O for support by submitting Device Support Request through this address:
http://www.dataio.com/support/dsr.asp

Bad Block Handling Type = "**Monahans_FX41_MIX**"

Spare area : Please refer to "Description of common NAND special features.pdf". ***Normally set as "Update ECC field" for this BBM, You can also set as "Enabled" if you want to use spare area data directly form the data file***.[Default 'Disabled']

MONFX41_MIX: Monahans blocks = the NUMBER of blocks in the front of the chip managed by Monahans scheme. Normally **6**.

MON: Number of Reserved blocks = the NUMBER of blocks reserved for Monahans scheme at the end of the chip. Normally **5**.

MON: OS start block = The first OS part block number. Normally **0.**

FlashFX: Header ECC = Whether use FlashFX ECC in FlashFX header pages or not. Normally **Disabled.**

The following special features are optional and can be ignored if default value works.

MON: Force filling FF to reserved bad block tables = whether fill the pages of the reserved bad block table to all 0xFF, this item is only required while these pages of customer data file is not blank. [Default 'Disabled']

MON: Next Block of Reserved Area = The NEXT block index after the reserved area. This item is used to specify the reserved area location and normally keep its value as the block amount of the device. [Default as device block amount]

Special Notes

Customer should prepare a universal data file includes the spare area contents. But, they can leave the ECC field bytes and the BB table existed page as all 0xFF.

Revision History

V1.0 June 23, 2011
Release this spec.

Appendix

You can get the file "Description of common NAND special features.pdf" from http://ftp.dataio.com/FCNotes/BBM/