4 Block Skip User Manual

General Description and Name

This bad blocking method divides the device up into "Logical Blocks".

In addition, there are two structures that must be written into the beginning of the device- the Primary and Secondary partition tables.

Relevant User Options

The following special features on the special features tab apply to this scheme. The default values might work in some cases but please make sure to set the right value according to your system.
Please note only the below special feature items are related to this scheme and ignore any others. If any of below items doesn't exist, please check whether the right version has been installed or contact Data I/O for support by submitting Device Support Request through this address:
http://www.dataio.com/support/dsr.asp

Bad Block Handling Type = "4 Block Skip"

Spare area : Please refer to "Description of common NAND special features.pdf". ***Normally set as " Enabled" or "Disabled" for this BBM***.[Default 'Disabled']

Required good block area: Start block = "0" Please refer to "Description of common NAND special features.pdf".

Required good block area: Number of blocks = "0" Please refer to "Description of common NAND special features.pdf".

Special Notes

The spare area in this scheme can either be programmed with the customer's image file, or it can be ignored. ECC is not an option with this particular scheme. However, the bad block marks are always located in the spare area (Byte 517 for x8 devices). Generally the customer's data file will contain the Spare Area information but it is not necessary.

Revision History

V1.0 June 12, 2009
Create this spec.

Appendix

You can get the file “Description of common NAND special features.pdf” from http://ftp.dataio.com/FCNotes/BBM/